

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,854,098 B2
DATED : February 8, 2005
INVENTOR(S) : Teig et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 45,

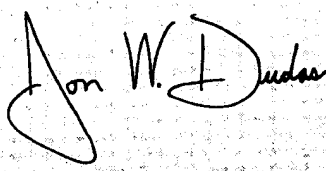
Line 31, after "identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks that are bounded to the technology," should read -- identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks that are bound to the technology, --

Column 46,

Line 9, after "wherein during an iteration, a first candidate sub-network is replace in the design with a first replacement sub-network, and during another iteration, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network." should read -- wherein during an iteration, a first candidate sub-network is replaced in the design with a first replacement sub-network, and during another iteration, a second candidate sub-network is replaced by a second replacement sub-network, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network. --

Signed and Sealed this

Thirty-first Day of May, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular grid background.

JON W. DUDAS

Director of the United States Patent and Trademark Office